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PATENT  
455610-2580.1

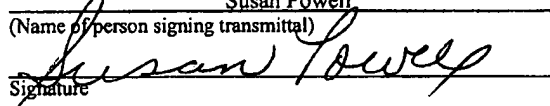
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Lawrence SALANT et al.  
Serial No. : 10/673,712  
Filed : September 29, 2003  
For : METHOD AND APPARATUS FOR BIT ERROR RATE ANALYSIS  
Examiner : GUERRIER, Merant  
Art Unit : 2117  
Confirmation No. : 2458

745 Fifth Avenue  
New York, NY 10151

**CERTIFICATE OF ELECTRONIC FILING**

I hereby certify that this correspondence is being transmitted via Electronic Filing Services on December 11, 2007.

Susan Powell  
(Name of person signing transmittal)  
  
Signature  
December 11, 2007  
Date of Signature

**AMENDMENT UNDER 37 CFR 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450,  
Alexandria, VA 22313-1450

Sir:

This is in response to the Final Office Action mailed June 25, 2007. The term for responding thereto is requested to be extended for three months to December 26, 2007. The requisite fee for this extension of time is enclosed. Please amend this application as follows: